



FPGA Design Engineer

Ref: MS-JDS-FDE-202208

Title: FPGA Design Engineer
Reports to: Project manager and Director
Location: Milan, Italy
Job purpose: Design of FPGA and RFSoc for radar sensor applications

Job duties:

- Design and implementation of algorithms on FPGA
- Design of FPGAs for high data throughput applications
- Define and develop verification environment
- Documentation of system design and test environment

Required skills and qualifications:

- Master's degree in Telecommunication/Aerospace/Electronic Engineering, or related fields
- 3+ years' experience with HDL design (VHDL/Verilog)
- Experience in developing test benches and simulation tools (Vivado simulator, ModelSim)
- Good knowledge of Vivado design flow
- Understanding of digital signal processing
- Knowledge of mathematical modelling techniques and related software (i.e. Matlab, Python, etc.)
- Knowledge of version control software such as Git
- Ability to think logically and to decompose a problem into its component parts
- Good knowledge of the English language for the working environment
- Good communication and team-working skills
- Enthusiasm, dynamicity, and self-motivation

Desired Skills/Qualifications

- Experience with Xilinx Zynq RFSoc embedded Linux programming is a plus
- Working knowledge of C/C++
- Working knowledge of TCL

Candidate must be eligible to work in the EU.



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Company description:

MetaSensing Srl is the Italian side of MetaSensing group, specialized in the design and manufacturing of Synthetic Aperture Radar systems for the Defence and Space sector.

MetaSensing is a small-medium enterprise focused on the development of advanced radar technology and systems, mainly Synthetic Aperture Radar (SAR), to offer customers cutting-edge solutions for their needs for intelligence, surveillance, reconnaissance, and defence.

Technology, hardware, and software, is developed in-house and is company-owned, making MetaSensing a consolidated reality with a broad portfolio of state-of-the-art radar solutions.

MetaSensing know-how covers the full chain of a radar development and production including requirements consolidation, system design and performance analysis, manufacturing, assembly, integration, testing including firmware and control, processing, and visualization software development.

With offices in Italy, Netherlands and Singapore and a network of business partners in many countries, MetaSensing offers customized high-tech radar solutions for spaceborne, airborne and ground-based platforms for multiple clients including R&D Centre, Universities, Government Agencies, and commercial players.

Employment condition: Full-time, 40 hours per week.

For additional information or for the application, please send an email with your CV and cover letter to joinus@metasensing.com with a subject Ref: MS-JDS-FDE-202208.

Milan, 22/08/2022